On Multiprocessor Utility Accrual Real-Time Scheduling With Statistical Timing Assurances

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Abstract

We present the first Utility Accrual (or UA) real-time scheduling algorithm for multiprocessors, called gMUA. The algorithm considers an application model where real-time activities are subject to time/utility function time constraints, variable execution time demands, and resource overloads where the total activity utilization demand exceeds the total capacity of all processors. We consider the scheduling objective of (1) probabilistically satisfying lower bounds on each activity's maximum utility and (2) maximizing the system-wide, total accrued utility. We establish several properties of gMUA including optimal total utility (for a special case), conditions under which individual activity utility lower bounds are satisfied, a lower bound on system-wide total accrued utility, and bounded sensitivity for assurances to variations in execution time demand estimates. Our simulation experiments validate our analytical results and confirm the algorithm's effectiveness and superiority.

Keywords: Time utility function, utility accrual scheduling, multiprocessor systems, statistical guarantees.

1 Introduction

Embedded real-time systems that are emerging in many domains such as robotic systems in the space domain (e.g., NASA/JPL's Mars Rover [9]) and control systems in the defense domain (e.g., airborne trackers [8]) are fundamentally distinguished by the fact that they operate in environments with dynamically uncertain properties. These uncertainties include transient and sustained resource overloads due to context-dependent activity execution times and arbitrary activity arrival patterns. Nevertheless, such systems' desire the strongest possible assurances on activity timeliness behavior. Another important distinguishing feature of these systems is their relatively long execution time magnitudes—e.g., in the order of milliseconds to minutes.

When resource overloads occur, meeting deadlines of all activities is impossible as the demand exceeds the supply. The urgency of an activity is typically orthogonal to the relative importance of the activity—-e.g., the most urgent activity can be the least important, and vice versa; the most urgent can be the most important, and vice versa. Hence when overloads occur, completing the most important activities irrespective of activity urgency is often desirable. Thus, a clear distinction has to be made between urgency and importance, during overloads. During under-loads, such a distinction need not be made, because deadline-based scheduling algorithms such as EDF are optimal (on one processor).

Deadlines by themselves cannot express both urgency and importance. Thus, we consider the abstraction of time/utility functions (or TUFs) [14] that express the utility of completing an application activity as a function of that activity's completion time. We specify deadline as a binary-valued, downward "step" shaped TUF; Figure 1(a) shows examples. Note that a TUF decouples importance and urgency—i.e., urgency is measured as a deadline on the X-axis, and importance is denoted by utility on the Y-axis.

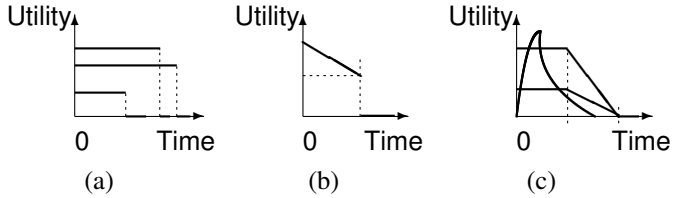


Figure 1: Example TUF Time Constraints: (a) Step TUFs; (b) AWACS TUF [8]; and (c) Coastal Air defense TUFs [17]

Many embedded real-time systems also have activities that are subject to *non-deadline* time constraints, such as those where the utility attained for activity completion *varies* (e.g., decreases, increases) with completion time. This is in contrast to deadlines, where a positive utility is accrued for completing the activity anytime before the deadline, after which zero, or infinitively negative utility is accrued. Figures 1(a)-1(c) show example such time constraints from two real applications (see [8] and references therein for application details). When activity time constraints are specified using TUFs, which subsume deadlines, the scheduling criteria is based on accrued utility, such as maximizing sum of the activities' attained utilities. We call such criteria, *utility accrual* (or UA) criteria, and scheduling algorithms that optimize them, as UA scheduling algorithms.

On single processors, UA algorithms that maximize total utility under step TUFs (see algorithms in [20]) default to EDF during under-loads, since EDF satisfies all deadlines during under-loads. Consequently, they obtain the maximum total utility during under-loads. During overloads, they favor more important activities (since more utility can be attained from them), irrespective of urgency. Thus, deadline scheduling's optimal timeliness behavior is a special-case of UA scheduling.

1.1 Scheduling on Multiprocessors

Multiprocessor architectures (e.g., Symmetric Multi-Processors or SMPs, Single Chip Heterogeneous Multiprocessors or SCHMs) are becoming more attractive for embedded systems primarily because major processor manufacturers (Intel, AMD) are making them decreasingly expensive. This makes such architectures very desirable for embedded system applications with high computational workloads, where additional, cost-effective processing capacity is often needed. Responding to this trend, RTOS vendors are increasingly providing multiprocessor platform support — e.g., QNX Neutrino is now available for a variety of SMP chips [19]. But this exposes the critical need for real-time scheduling for multiprocessors — a comparatively undeveloped area of real-time scheduling which has recently received significant research attention, but is not yet well supported by the RTOS products. Consequently, the impact of cost-effective multiprocessor platforms for embedded systems remains nascent.

One unique aspect of multiprocessor scheduling is the degree of run-time migration that is al-

lowed for job instances of a task across processors (at scheduling events). Example migration models include: (1) *full migration*, where jobs are allowed to arbitrarily migrate across processors during their execution. This usually implies a global scheduling strategy, where a single shared scheduling queue is maintained for all processors and a processor-wide scheduling decision is made by a single (global) scheduling algorithm; (2) *no migration*, where tasks are statically (off-line) partitioned and allocated to processors. At run-time, job instances of tasks are scheduled on their respective processors by processors' local scheduling algorithm, like single processor scheduling; and (3) *restricted migration*, where some form of migration is allowed—e.g., at job boundaries.

Carpentar *et al.* [6] have catalogued multiprocessor real-time scheduling algorithms considering the degree of job migration. The Pfair class of algorithms [4] that allow full migration have been shown to achieve a schedulable utilization bound (below which all tasks meet their deadlines) that equals the total capacity of all processors — thus, they are theoretically optimal. However, Pfair algorithms incur significant overhead due to their quantum-based scheduling approach [10]. Thus, scheduling algorithms other than Pfair (e.g., global EDF) have also been studied though their schedulable utilization bounds are lower.

Global EDF scheduling on multiprocessors is subject to the "Dhall effect" [11], where a task set with total utilization arbitrarily close to 1.0 cannot be scheduled satisfying all deadlines. To overcome this, researchers have studied global EDF's behavior under restricted individual task utilizations. For example, on M processors, Srinivasan and Baruah show that when the maximum individual task utilization, u_{max}, is bounded by $M/(2M-1)$, EDF's utilization bound is $M^2/(2M-1)$ [22]. In [12], Goossens *et. al* show that EDF's utilization bound is $M - (M-1)\,u_{max}$. This work was later extended by Baker for the more general case of deadlines less than or equal to periods in [3]. In [5], Bertogna *et al.* show that Baker's utilization bound does not dominate the bound of Goossens *et. al*, and vice versa.

While most of these past works focus on the hard real-time objective of always meeting all deadlines, recently, there has been efforts that consider the objective of bounding the tardiness of tasks. In [21], Srinivasan and Anderson derive a tardiness bound for a suboptimal Pfair scheduling algo-

rithm. In [1], for a restricted migration model, where migration is allowed only at job boundaries, Andersen *et. al* present an EDF-based partitioning scheme and scheduling algorithm that ensures bounded tardiness. In [10], Devi and Anderson derive the tardiness bounds for global EDF when the total utilization of a task system may equal the number of available processors.

1.2 Contributions

In this paper, we consider the problem of global UA scheduling on an SMP system with M number of identical processors. We consider global scheduling (as opposed to partitioned scheduling) because of its improved schedulability and flexibility [13]. Further, in many embedded architectures (e.g., those with no cache), its migration overhead has a lower impact on performance [5]. Moreover, applications of interest to us [8, 9] are often subject to resource overloads, during when the total application utilization demand exceed the total processing capacity of all processors. When that happens, we hypothesize that global scheduling can yield greater scheduling flexibility, resulting in greater accrued activity utility, than partitioned scheduling.

We consider repeatedly occurring application activities (e.g., tasks) that are subject to TUF time constraints, variable execution times, and overloads. To account for uncertainties in activity execution behaviors, we consider a stochastic model, where activity execution demand is stochastically expressed. Activities repeatedly arrive with a known minimum inter-arrival time. For such a model, our objective is to: (1) provide statistical assurances on individual activity timeliness behavior including probabilistically-satisfied lower bounds on each activity's maximum utility; (2) provide assurances on system-level timeliness behavior including assured lower bound on the sum of activities' attained utilities; and (3) maximize the sum of activities' attained utilities.

This problem has not been studied in the past and is $\mathcal{NP}$-hard. We present a polynomial-time, heuristic algorithm for the problem called the *global Multiprocessor Utility Accrual scheduling algorithm* (or gMUA). We establish several properties of gMUA including optimal total utility for the special case of step TUFs and application utilization demand not exceeding global EDF's utilization bound, conditions under which individual activity utility lower bounds are satisfied, and a lower bound on system-wide total accrued utility. We also show that the algorithm's assurances

have bounded sensitivity to variations in execution time demand estimates, in the sense that the assurances hold as long as the variations satisfy a sufficient condition that we present. Further, we show that the algorithm is robust against a variant of the Dhall effect.

Therefore, the contribution of the paper is the gMUA algorithm. To the best of our knowledge, we are not aware of any other efforts that solve the problem solved by gMUA.

The rest of the paper is organized as follows: Section 2 describes our models and scheduling objective. In Section 3, we discuss the rationale behind gMUA and present the algorithm. We describe the algorithm's properties in Section 4 and report our simulation-based experimental studies in Section 5. The paper concludes in Section 6.

2 Models and Objective

2.1 Activity Model

We consider the application to consist of a set of tasks, denoted **T**=$\{T_1, T_2, ..., T_n\}$. Each task T_i has a number of instances, called jobs, and these jobs may be released either periodically or sporadically with a known minimal inter-arrival time. The j^{th} job of task T_i is denoted as $J_{i,j}$. The period or minimal inter-arrival time of a task T_i is denoted as P_i. All tasks are assumed to be independent, i.e., they do not share any resource or have any precedences. The basic scheduling entity that we consider is the job abstraction. Thus, we use J to denote a job without being task specific, as seen by the scheduler at any scheduling event.

A job's time constraint is specified using a TUF. Jobs of the same task have the same TUF. We use $U_i()$ to denote the TUF of task T_i. Thus, completion of the job $J_{i,j}$ at time t will yield an utility of $U_i(t)$.

TUFs can be classified into unimodal and multimodal functions. Unimodal TUFs are those for which any decrease in utility cannot be followed by an increase. Figure 1 shows examples. TUFs which are not umimodal are multimodal. In this paper, we focus on *non-increasing* unimodal TUFs, as they encompass majority of the time constraints in our motivating applications.

Each TUF U_i of $J_{i,j}$ has an initial time $I_{i,j}$ and a termination time $X_{i,j}$. Initial and termination times are the earliest and the latest times for which the TUF is defined, respectively. We assume

that $I_{i,j}$ is the arrival time of job $J_{i,j}$, and $X_{i,j} - I_{i,j}$ is the period or minimal inter-arrival time P_i of the task T_i. If $J_{i,j}$'s $X_{i,j}$ is reached and execution of the corresponding job has not been completed, an exception is raised, and the job is aborted.

2.2 Job Execution Time Demands

We estimate the statistical properties, e.g., distribution, mean, variance, of job execution time demand rather than the worst-case demand because: (1) applications of interest to us [8, 9] exhibit a large variation in their *actual workload*. Thus, the statistical estimation of the demand is much more stable and hence more predictable than the actual workload; (2) worst-case workload is usually a very conservative prediction of the actual workload [2], resulting in resource over-supply; and(3) allocating execution times based on the statistical estimation of tasks' demands can provide statistical performance assurances, which is sufficient for our motivating applications.

Let Y_i be the random variable of a task T_i's execution time demand. Estimating the execution time demand distribution of the task involves two steps: (1) profiling its execution time usage, and (2) deriving the probability distribution of that usage. A number of measurement-based, off-line and online profiling mechanisms exist (e.g., [24]). We assume that the mean and variance of Y_i are finite and determined through either online or off-line profiling.

We denote the *expected* execution time demand of a task T_i as $E(Y_i)$, and the variance on the demand as $Var(Y_i)$.

2.3 Statistical Timeliness Requirement

We consider a task-level statistical timeliness requirement: Each task must accrue some percentage of its maximum possible utility with a certain probability. For a task T_i, this requirement is specified as $\{\nu_i, \rho_i\}$, which implies that T_i must accrue at least ν_i percentage of its maximum possible utility with the probability ρ_i. This is also the requirement of each job of T_i. Thus, for example, if $\{\nu_i, \rho_i\} = \{0.7, 0.93\}$, then T_i must accrue at least 70% of its maximum possible utility with a probability no less than 93%. For step TUFs, ν can only take the value 0 or 1. Note that the objective of always satisfying all task deadlines is the special case of $\{\nu_i, \rho_i\} = \{1.0, 1.0\}$.

This statistical timeliness requirement on the utility of a task implies a corresponding requirement on the range of task sojourn times. Since we focus on non-increasing unimodal TUFs, upper-bounding task sojourn times will lower-bound task utilities.

2.4 Scheduling Objective

We consider a two-fold scheduling criterion: (1) assure that each task T_i accrues the specified percentage ν_i of its maximum possible utility with at least the specified probability ρ_i; and (2) maximize the system-level total attained utility. We also desire to obtain a lower bound on the system-level total attained utility. Also, when it is not possible to satisfy ρ_i for each task (e.g., due to overloads), our objective is to maximize the system-level total utility.

This problem is $\mathcal{NP}$-hard because it subsumes the $\mathcal{NP}$-hard problem of scheduling dependent tasks with step TUFs on one processor [7].

3 The gMUA Algorithm

3.1 Bounding Accrued Utility

Let $s_{i,j}$ be the sojourn time of the j^{th} job of task T_i, where the sojourn time is defined as the period from the job's release to its completion. Now, task T_i's statistical timeliness requirement can be represented as $Pr(U_i(s_{i,j}) \geq \nu_i \times U_i^{max}) \geq \rho_i$. Since TUFs are assumed to be non-increasing, it is sufficient to have $Pr(s_{i,j} \leq D_i) \geq \rho_i$, where D_i is the upper bound on the sojourn time of task T_i. We call D_i "critical time" hereafter, and it is calculated as $D_i = U_i^{-1}(\nu_i \times U_i^{max})$, where $U_i^{-1}(x)$ denotes the inverse function of TUF $U_i()$. Thus, T_i is (probabilistically) assured to accrue at least the utility percentage $\nu_i = U_i(D_i)/U_i^{max}$, with the probability ρ_i.

Note that the period or minimum inter-arrival time P_i and the critical time D_i of the task T_i have the following relationships: (1) $P_i = D_i$ for a binary-valued, downward step TUF; and (2) $P_i \geq D_i$, for other non-increasing TUFs.

3.2 Bounding Utility Accrual Probability

Since task execution time demands are stochastically specified (through means and variances), we need to determine the actual execution time that must be allocated to each task, such that the desired utility accrual probability ρ_i is satisfied. Further, this execution time allocation must account for the uncertainty in the execution time demand specification (i.e., the variance factor).

Given the mean and the variance of a task T_i's execution time demand Y_i, by a one-tailed version of the Chebyshev's inequality, when $y \geq E(Y_i)$, we have:

$$Pr[Y_i < y] \geq \frac{(y - E(Y_i))^2}{Var(Y_i) + (y - E(Y_i))^2} \tag{1}$$

From a probabilistic point of view, Equation 1 is the direct result of the cumulative distribution function of task T_i's execution time demands—i.e., $F_i(y) = Pr[Y_i \leq y]$. Recall that each job of task T_i must accrue ν_i percentage of its maximum utility with a probability ρ_i. To satisfy this requirement, we let $\rho_i' = \frac{(C_i - E(Y_i))^2}{Var(Y_i) + (C_i - E(Y_i))^2} \geq \rho_i$ and obtain the minimum required execution time $C_i = E(Y_i) + \sqrt{\frac{\rho_i' \times Var(Y_i)}{1 - \rho_i'}}$.

Thus, the gMUA algorithm allocates C_i execution time units to each job $J_{i,j}$ of task T_i, so that the probability that job $J_{i,j}$ requires no more than the allocated C_i execution time units is at least ρ_i—i.e., $Pr[Y_i < C_i] \geq \rho_i' \geq \rho_i$. We set $\rho_i' = (\max\{\rho_i\})^{\frac{1}{n}}, \forall i$ to satisfy requirements. Supposing that each task is allocated C_i time within its P_i, the actual demand of each task often vary. Some jobs of the task may complete its execution before using up its allocated time and the others may not. gMUA probabilistically schedules the jobs of a task T_i to provide assurance ρ_i' ($\geq \rho_i$) as long as they are satisfying a certain schedulability test.

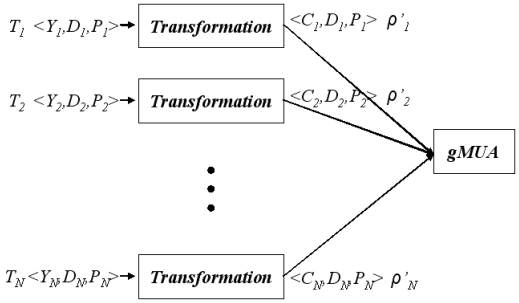


Figure 2: Transformation Array and gMUA

Figure 2 shows our method of transforming the stochastic execution time demand ($E(Y_i)$ and $Var(Y_i)$) into execution time allocation C_i. Note that this transformation is independent of our proposed scheduling algorithm.

3.3 Algorithm Description

gMUA's scheduling events include job arrival and job completion. To describe gMUA, we define the following variables and auxiliary functions:

- ζ_r: current job set in the system including running jobs and unscheduled jobs.
- σ_{tmp}, σ_a: a temporary schedule; σ_m: schedule for processor m, where $m \leq M$.
- $J_k.C(t)$: J_k's remaining allocated execution time.
- `offlineComputing()` is computed at time $t = 0$ once. For a task T_i, it computes C_i as $C_i = E(Y_i) + \sqrt{\frac{\rho_i \times Var(Y_i)}{1-\rho_i}}$.
- `UpdateRAET(`ζ_r`)` updates the remaining allocated execution time of all jobs in the set ζ_r.
- `feasible(`σ`)` returns a boolean value denoting schedule σ's feasibility; `feasible(`J_k`)` denotes job J_k's feasibility. For σ (or J_k) to be feasible, the predicted completion time of each job in σ (or J_k), must not exceed its critical time.
- `sortByECF(`σ`)` sorts jobs of σ in the order of earliest critical time first.
- `findProcessor()` returns the ID of the processor on which the currently assigned tasks have the shortest sum of allocated execution times.
- `append(`J_k`,`σ`)` appends job J_k at rear of schedule σ.
- `remove(`J_k`,`σ`)` removes job J_k from schedule σ.
- `removeLeastPUDJob(`σ`)` removes job with the least *potential utility density* (or PUD) from schedule σ. PUD is the ratio of the expected job utility (obtained when job is immediately executed to completion) to the remaining job allocated execution time, i.e., PUD of a job J_k is $\frac{U_k(t+J_k.C(t))}{J_k.C(t)}$. Thus, PUD measures the job's "return on investment." Function returns the removed job.
- `headOf(`σ_m`)` returns the set of jobs that are at the head of schedule σ_m, $1 \leq m \leq M$.

Algorithm 1: gMUA

Input : **T**=$\{T_1,...,T_n\}$, ζ_r=$\{J_1,...,J_N\}$, M:# of processors
Output : array of dispatched jobs to processor p, Job_p
Data: $\{\sigma_1, ..., \sigma_M\}$, σ_{tmp}, σ_a
`offlineComputing`(**T**);
Initialization: $\{\sigma_1, ..., \sigma_M\} = \{0, ..., 0\}$;
`UpdateRAET`(ζ_r);
for $\forall J_k \in \zeta_r$ **do**
    $J_k.PUD = \frac{U_k(t+J_k.C(t))}{J_k.C(t)}$;
σ_{tmp} = `sortByECF`(ζ_r);
for $\forall J_k \in \sigma_{tmp}$ *from head to tail* **do**
    if $J_k.PUD > 0$ **then**
        m = `findProcessor`();
        `append`(J_k, σ_m);
for $m = 1$ **to** M **do**
    $\sigma_a = null$;
    while *!`feasible`(σ_m) and !`IsEmpty`(σ_m)* **do**
        t = `removeleastPUD`(σ_m);
        `append`(t, σ_a);
    `sortByECF`(σ_a);
    σ_m += σ_a;
$\{Job_1, ..., Job_M\}$ = `headOf`($\{\sigma_1, ..., \sigma_M\}$);
return $\{Job_1, ..., Job_M\}$;

A description of gMUA at a high level of abstraction is shown in Algorithm 1. The procedure `offlineComputing()` is included in line 4, although it is executed only once at $t = 0$. When gMUA is invoked, it updates the remaining allocated execution time of each job. The remaining allocated execution times of running jobs are decreasing, while those of unscheduled jobs remain constant. The algorithm then computes the PUDs of all jobs.

The jobs are then sorted in the order of earliest critical time first (or ECF), in line 9. In each step of the for loop from line 10 to line 13, the job with the earliest critical time is selected to be assigned to a processor. The processor that yields the shortest sum of allocated execution times of all jobs in its local schedule is selected for assignment (procedure `findProcessor()`). The rationale for this choice is that the shortest summed execution time processor results in the nearest scheduling event for completing a job after assigning each job, which is to establish the same schedule as the global EDF does. Then, the job J_k with the earliest critical time is inserted into the local schedule σ_m of the selected processor m.

In the for-loop from line 14 to line 20, gMUA attempts to make each local schedule feasible by removing the lowest PUD job. In line 16, if σ_m is not feasible, then gMUA removes the job with the least PUD from σ_m until σ_m becomes feasible. All removed jobs are temporarily stored in a schedule σ_a and then appended to each σ_m in ECF order. Note that simply aborting the removed jobs may result in decreased accrued utility. This is because, the algorithm may decide to remove a job which is estimated to have a longer allocated execution time than its actual one, even though it may be able to accrue utility. For this case, gMUA gives the job another chance to be scheduled instead of aborting it, which eventually makes the algorithm more robust. Finally, each job at the head of $\sigma_m, 1 \leq m \leq M$ is selected for execution on the respective processor.

gMUA's time cost depends upon that of procedures `sortByECF()`, `findprocessor()`, `append()`, `feasible()`, and `removeLeastPUDJob()`. With n tasks, `sortByECF()` costs $O(nlogn)$. For SMPs with restricted number of processors, `findprocessor()`'s costs $O(M)$. While `append()` costs $O(1)$ time, both `feasible()` and `removeLeastPUDJob()` costs $O(n)$. The while-loop in line 16 iterates at most n times, costing the entire loop $O(n^2)$. Thus, the algorithm costs $O(Mn^2)$. However, M of SMPs is usually small (e.g., 16) and bounded with

respect to the problem size of number of tasks. Thus, gMUA costs $O(n^2)$.

gMUA's $O(n^2)$ cost is similar to that of many past UA algorithms [20]. Our prior implementation experience with UA scheduling at the middleware-level have shown that the overheads are in the magnitude of sub-milliseconds [16] (sub-microsecond overheads may be possible at the kernel-level). We anticipate a similar overhead magnitude for gMUA. Though this cost is higher than that of many traditional algorithms, the cost is justified for applications with longer execution time magnitudes (e.g., milliseconds to minutes) such as those that we focus here. Of course, this high cost cannot be justified for every application.[1]

4 Algorithm Properties

4.1 Timeliness Assurances

We establish gMUA's timeliness assurances under the conditions of (1) independent tasks that arrive periodically, and (2) task utilization demand satisfies any of the schedulable utilization bounds for global EDF (GFB, BAK, BCL) in [5].

Theorem 1 (Optimal Performance with Step Shaped TUFs)**.** *Suppose that only step shaped TUFs are allowed under conditions (1) and (2). Then, a schedule produced by global EDF is also produced by gMUA, yielding equal total utilities. This is a critical time-ordered schedule.*

Proof. We prove this by examining Algorithm 1. In line 9, the queue σ_{tmp} is sorted in a non-decreasing critical time order. In line 12, the function `findProcessor()` returns the index of the processor on which the summed execution time of assigned tasks is the shortest among all processors. Assume that there are n tasks in the current ready queue. We consider two cases: (1) $n \leq M$ and (2) $n > M$. When $n \leq M$, the result is trivial — gMUA's schedule of tasks on each processor is identical to that produced by EDF (every processor has a single task or none assigned). When $n > M$, task T_i ($M < i \leq n$) will be assigned to the processor whose tasks have the shortest summed execution time. This implies that this processor will have the earliest completion for all assigned tasks up to T_{i-1}, so that the event that will assign T_i will occur by this completion. Note that tasks in σ_{tmp} are selected to be assigned to processors according to ECF. This is precisely the global EDF schedule, as we consider a critical time of UA scheduling as a deadline of traditional hard real-time scheduling. Under conditions (1) and (2), EDF meets all deadlines. Thus, each processor always has a feasible schedule, and the if-block from line 16 to line 18 will never be executed. Thus, gMUA produces the same schedule as global EDF. □

[1] When UA scheduling is desired with low overhead, solutions and tradeoffs exist. Examples include linear-time stochastic UA scheduling [15], and using special-purpose hardware accelerators for UA scheduling (analogous to floating-point co-processors) [18].

Some important corollaries about gMUA's timeliness behavior can be deduced from EDF's behavior under conditions (1) and (2).

Corollary 2. *Under conditions (1) and (2), gMUA always completes the allocated execution time of all tasks before their critical times.*

Theorem 3 (Statistical Task-Level Assurance)**.** *Under conditions (1) and (2), gMUA meets the statistical timeliness requirement* $\{\nu_i, \rho_i\}$ *for each task* T_i*.*

Proof. From Corollary 2, all allocated execution times of tasks can be completed before their critical times. Further, based on the results of Equation 1, among the actual processor time of task T_i's jobs, at least ρ_i of them have lesser actual execution time than the allocated execution time. Thus, gMUA can satisfy at least ρ_i critical times—i.e., the algorithm accrues ν_i utility with a probability of at least ρ_i. □

Theorem 4 (System-Level Utility Assurance)**.** *Under conditions (1) and (2), if a task* T_i*'s TUF has the highest height* U_i^{max}*, then the system-level utility ratio, defined as the utility accrued by gMUA with respect to the system's maximum possible utility, is at least* $\frac{\rho_1\nu_1 U_1^{max}/P_1+...+\rho_n\nu_n U_n^{max}/P_n}{U_1^{max}/P_1+...+U_n^{max}/P_n}$.

Proof. We denote the number of jobs released by task T_i as m_i. Each m_i is computed as $\frac{\Delta t}{P_i}$, where Δt is a time interval. Task T_i can accrue at least ν_i percentage of its maximum possible utility with the probability ρ_i. Thus, the ratio of the system-level accrued utility to the system's maximum possible utility is $\frac{\rho_1\nu_1 U_1^{max}m_1+...+\rho_n\nu_n U_n^{max}m_n}{U_1^{max}m_1+...+U_n^{max}m_n}$. Thus, the formula comes to $\frac{\rho_1\nu_1 U_1^{max}/P_1+...+\rho_n\nu_n U_n^{max}/P_n}{U_1^{max}/P_1+...+U_n^{max}/P_n}$. □

4.2 Dhall Effect

The *Dhall effect* [11] shows that there exists a task set that requires nearly 1 total utilization demand, but cannot be scheduled to meet all deadlines under global EDF and RM even with infinite number of processors. Prior research has revealed that this is caused by the poor performance of global EDF and RM when the task set contains both high utilization tasks and low utilization tasks together. This phenomena, in general, can also affect UA scheduling algorithms' performance, and counter such algorithms' ability to maximize the total attained utility. We discuss this with an example inspired from [23]. We consider the case when the execution time demands of all tasks are constant with no variance, and gMUAi estimates them accurately.

Example A. Consider $M + 1$ periodic tasks that are scheduled on M processors under global EDF. Let task τ_i, where $1 \leq i \leq M$, have $P_i = D_i = 1, C_i = 2\epsilon$, and task τ_{M+1} have $P_{M+1} = D_{M+1} = 1 + \epsilon, C_{M+1} = 1$. We assume that each task τ_i has a step shaped TUF with height h_i

and task τ_{M+1} has a step shaped TUF with height H_{M+1}. When all tasks arrive at the same time, tasks τ_i will execute immediately and complete their execution 2ϵ time units later. Task τ_{M+1} then executes from time 2ϵ to time $1 + 2\epsilon$. Since task τ_{M+1}'s critical time — we assume here it is the same as its period — is $1 + \epsilon$, it begins to miss its critical time. By letting $M \to \infty$, $\epsilon \to 0$, $h_i \to 0$ and $H_{M+1} \to \infty$, we have a task set, whose total utilization demand is near 1 and the maximum possible total attained utility is infinite, but that finally accrues zero total utility even with infinite number of processors.

We call this phenomena as the *UA Dhall effect*. Conclusively, one of the reasons why global EDF is inappropriate as a UA scheduler is that it is prone to suffer this effect. However, gMUA overcomes this phenomena.

Example B. Consider the same scenario as in Example A, but now, let the task set be scheduled by gMUA. In Algorithm 1, gMUA first tries to schedule tasks like global EDF, but it will fail to do so as we saw in Example A. When gMUA finds that τ_{M+1} will miss its critical time on processor m (where $1 \leq m \leq M$), the algorithm will select a task with lower PUD on processor m for removal. On processor m, there should be two tasks, τ_m and τ_{M+1}. τ_m is one of τ_i where $1 \leq i \leq M$. When letting $h_i \to 0$ and $H_{M+1} \to \infty$, the PUD of task τ_m is almost zero and that of task τ_{M+1} is infinite. Therefore, gMUA removes τ_m and eventually accrues infinite utility as expected.

Under the case when *Dhall effect* occurs, we can establish *UA Dhall effect* by assigning extremely high utility to the task that will be removed by global EDF. In this sense, *UA Dhall effect* is a special case of the *Dhall effect*. It also implies that the scheduling algorithm suffering from *Dhall effect* will likely suffer from *UA Dhall effect*, when it schedules the tasks that are subject to TUF time constraints.

The fact that gMUA is more robust against *UA Dhall effect* than global EDF can be observed in our simulation experiments (see Section 5).

4.3 Sensitivity of Assurances

gMUA is designed under the assumption that task expected execution time demands and the variances on the demands — i.e., the algorithm inputs $E(Y_i)$ and $Var(Y_i)$ – are correct. How-

ever, it is possible that these inputs may have been miscalculated (e.g., due to errors in application profiling) or that the input values may change over time (e.g., due to changes in application's execution context). To understand gMUA's behavior when this happens, we assume that the expected execution time demands, $E(Y_i)$'s, and their variances, $Var(Y_i)$'s, are erroneous, and develop the sufficient condition under which the algorithm is still able to meet $\{\nu_i, \rho_i\}$ for all tasks T_i.

Let a task T_i's correct expected execution time demand be $E(Y_i)$ and its correct variance be $Var(Y_i)$, and let an erroneous expected demand $E'(Y_i)$ and an erroneous variance $Var'(Y_i)$ be specified as the input to gMUA. Let the task's statistical timeliness requirement be $\{\nu_i, \rho_i\}$. We show that if gMUA can satisfy $\{\nu_i, \rho_i\}$ with the correct expectation $E(Y_i)$ and the correct variance $Var(Y_i)$, then there exists a sufficient condition under which the algorithm can still satisfy $\{\nu_i, \rho_i\}$ even with the incorrect expectation $E'(Y_i)$ and incorrect variance $Var'(Y_i)$.

Theorem 5. *Assume that gMUA satisfies $\{\nu_i, \rho_i\}, \forall i$, under correct, expected execution time demand estimates, $E(Y_i)$'s, and their correct variances, $Var(Y_i)$'s. When incorrect expected values, $E'(Y_i)$'s, and variances, $Var'(Y_i)$'s, are given as inputs instead of $E(Y_i)$'s and $Var(Y_i)$'s, gMUA satisfies $\{\nu_i, \rho_i\}, \forall i$, if $E'(Y_i) + (C_i - E(Y_i))\sqrt{\frac{Var'(Y_i)}{Var(Y_i)}} \geq C_i, \forall i$, and the task execution time allocations, computed using $E'(Y_i)$'s and $Var'(Y_i)$, satisfy any of the schedulable utilization bounds for global EDF.*

Proof. We assume that if gMUA has correct $E(Y_i)$'s and $Var(Y_i)$'s as inputs, then it satisfies $\{\nu_i, \rho_i\}, \forall i$. This implies that the C_i's determined by Equation 1 are feasibly scheduled by gMUA satisfying all task critical times:

$$\rho_i = \frac{(C_i - E(Y_i))^2}{Var(Y_i) + (C_i - E(Y_i))^2}. \tag{2}$$

However, gMUA has incorrect inputs, $E'(Y_i)$'s and $Var'(Y_i)$, and based on those, it determines C_i's by Equation 1 to obtain the probability $\rho_i, \forall i$:

$$\rho_i = \frac{(C_i' - E'(Y_i))^2}{Var'(Y_i) + (C_i' - E'(Y_i))^2}. \tag{3}$$

Unfortunately, C_i' that is calculated from the erroneous $E'(Y_i)$ and $Var'(Y_i)$ leads gMUA to another probability ρ_i' by Equation 1. Thus, although we expect assurance with the probability ρ_i, we can only obtain assurance with the probability ρ_i' because of the error. ρ' is given by:

$$\rho_i' = \frac{(C_i' - E(Y_i))^2}{Var(Y_i) + (C_i' - E(Y_i))^2}. \tag{4}$$

Note that we also assume that tasks with C_i' satisfy the global EDF's utilization bound; otherwise gMUA cannot provide the assurances. To satisfy $\{\nu_i, \rho_i\}, \forall i$, the actual probability ρ_i' must be greater than the desired probability ρ_i. Since $\rho_i' \geq \rho_i$,

$$\frac{(C_i' - E(Y_i))^2}{Var(Y_i) + (C_i' - E(Y_i))^2} \geq \frac{(C_i - E(Y_i))^2}{Var(Y_i) + (C_i - E(Y_i))^2}.$$

Hence, $C' \geq C_i$. From Equations 2 and 3,

$$C_i' = E'(Y_i) + (C_i - E(Y_i))\sqrt{\frac{Var'(Y_i)}{Var(Y_I)}} \geq C_i. \tag{5}$$

□

Corollary 6. *Assume that gMUA satisfies $\{\nu_i, \rho_i\}, \forall i$, under correct, expected execution time demand estimates, $E(Y_i)$'s, and their correct variances, $Var(Y_i)$'s. When incorrect expected values, $E'(Y_i)$'s, are given as inputs instead of $E(Y_i)$'s but with correct variances $Var(Y_i)$, gMUA satisfies $\{\nu_i, \rho_i\}, \forall i$, if $E'(Y_i) \geq E(Y_i), \forall i$, and the task execution time allocations, computed using $E'(Y_i)$'s, satisfy the schedulable utilization bound for global EDF.*

Proof. This can be proved by replacing $Var'(Y_i)$ with $Var(Y_i)$ in Equation 5. □

Corollary 6, a special case of Theorem 5, is intuitively straightforward: It essentially states that if overestimated demands are schedulable, then gMUA can still satisfy $\{\nu_i, \rho_i\}, \forall i$. Thus, it is desirable to specify larger $E'(Y_i)$s as input to the algorithm when there is the possibility of errors in determining the expected demands, or when the expected demands may vary with time.

Corollary 7. *Assume that gMUA satisfies $\{\nu_i, \rho_i\}, \forall i$, under correct, expected execution time demand estimates, $E(Y_i)$'s, and their correct variances, $Var(Y_i)$'s. When incorrect variances, $Var'(Y_i)$'s, are given as inputs instead of correct $Var(Y_i)$'s but with correct expectations $E(Y_i)$'s, gMUA satisfies $\{\nu_i, \rho_i\}, \forall i$, if $Var'(Y_i) \geq Var(Y_i), \forall i$, and the task execution time allocations, computed using $E'(Y_i)$'s, satisfy the schedulable utilization bound for global EDF.*

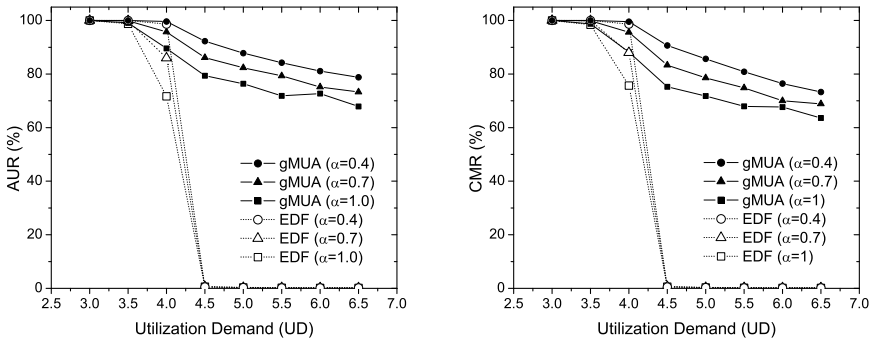
5 Experimental Evaluation

We conducted simulation-based experimental studies to validate our analytical results and to compare gMUA's performance with global EDF. We consider two cases: (1) the demand of all tasks are constant (i.e., no variance) and gMUA exactly estimates the execution time allocation, and (2) the demand of all tasks statistically varies and gMUA probabilistically estimates the execution time allocation for each task. The former experiment is conducted to evaluate gMUA's generic performance as opposed to EDF, while the latter is conducted to validate the algorithm's assurances.

5.1 Performance with Constant Demand

We consider an SMP machine with 4 processors. A task T_i's period $P_i(= X_i)$ and its expected execution time $E(Y_i)$ are randomly generated in the range [1,30] and [1, $\alpha \cdot P_i$], respectively, where α is defined as $max\{\frac{C_i}{P_i} | i = 1, ..., n\}$ and $Var(Y_i)$ are zero. According to [12], EDF's schedulable utilization bound depends on α as well as the number of processors. It implies that no matter how many processors the system has, there exists task sets with total utilization demand (UD) close to 1.0, which cannot be scheduled under EDF satisfying all deadlines. Generally, the performance of global schemes tends to decrease when α increases.

We consider two TUF shape patterns: (1) all tasks have step shaped TUFs, and (2) a heterogeneous TUF class, including step, linearly decreasing and parabolic shapes. Each TUF's height is randomly generated in the range [1,100].



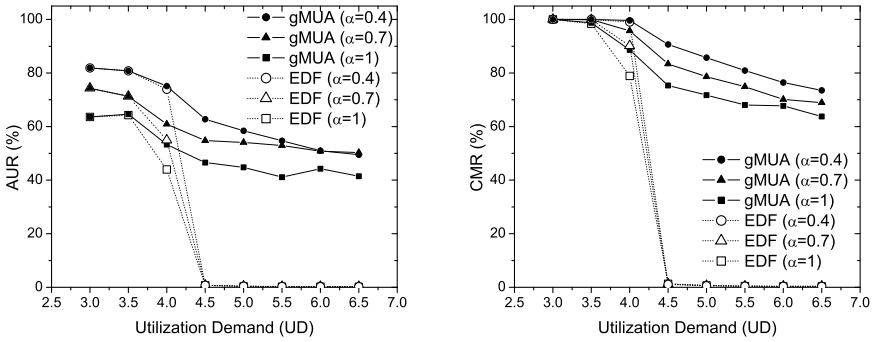
(a) AUR (b) CMR

Figure 3: Performance Under Constant Demand, Step TUFs

The number of tasks are determined depending on the given UD and the α value. We vary the UD from 3 to 6.5, including the case where it exceeds the number of processors. We set α to 0.4, 0.7, and 1. For each experiment, more than 1000,000 jobs are released. To see the generic performance of gMUA, we assume $\{\nu_i, \rho_i\} = \{0, 1\}$.

Figures 3 and 4 show the accrued utility ratio (AUR) and critical-time meet ratio (CMR) of gMUA and EDF, respectively, under increasing UD (from 3.0 to 6.5) and for the three α values. AUR is the ratio of total accrued utility to the total maximum possible utility, and CMR is the ratio of the number of jobs meeting their critical times to the total number of job releases. For a task

with a step TUF, its AUR and CMR are identical. But the system-level AUR and CMR can be different due to the mix of different utility of tasks.



(a) AUR (b) CMR

Figure 4: Performance Under Constant Demand, Heterogeneous TUFs

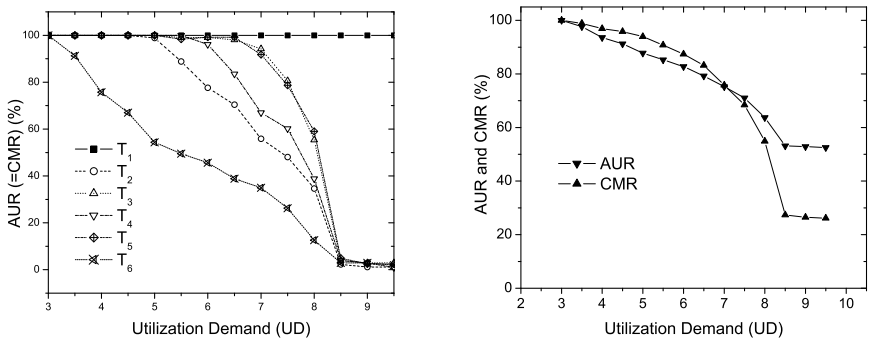
When all tasks have step TUFs and the total UD satisfies the global EDF's schedulable utilization bound, gMUA performs exactly the same to EDF. This validates Theorem 1.

EDF's performance drops sharply after $UD = 4.0$ (for step TUFs), which corresponds to the number of processors in our experiments. This is due to EDF's domino effect (originally identified for single processors) that occurs here, when UD exceeds the number of processors. On the other hand, the performance of gMUA gracefully degrades as UD increases and exceeds 4.0, since gMUA selects favors as many feasible, higher PUD tasks as possible, instead of simply favoring earlier deadline tasks.

Observe that EDF begins to miss deadlines much earlier than when $UD = 4.0$, as indicated in [5]. Even when $UD < 4.0$, gMUA outperforms EDF in both AUR and CMR. This is because gMUA is likely to find a feasible or at least better schedule even when EDF cannot find a feasible one, as we have seen in Section 4.2.

We also observe that α affects the AUR and CMR of both EDF and gMUA. Despite this effect, gMUA outperforms EDF for the same α and UD for the reason that we describe above.

We observe similar and consistent trends for tasks with heterogeneous TUFs in Figure 4. The figure shows that gMUA is superior to EDF under heterogeneous TUFs and when UD exceeds the number of processors.



(a) Task-level (b) System-level

Figure 5: Performance Under Statistical Demand, Step TUFs

5.2 Performance with Statistical Demand

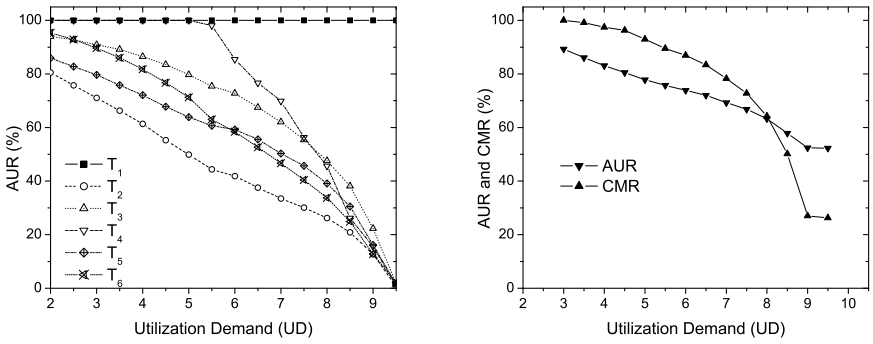
We now evaluate gMUA's statistical timeliness assurances. The task settings used in our simulation study are summarized in Table 1. The table shows the task periods and the maximum utility (or U_{max}) of the TUFs. For each task T_i's demand Y_i, we generate normally distributed execution time demands. Task execution times are changed along with the total UD. We consider both step and heterogeneous TUF shapes as before.

Table 1: Task Settings

Task	P_i	U_i^{max}	ρ_i	$E(Y_i)$	$Var(Y_i)$
T_1	25	400	0.96	3.15	0.01
T_2	28	100	0.96	13.39	0.01
T_3	49	20	0.96	18.43	0.01
T_4	49	100	0.96	23.91	0.01
T_5	41	30	0.96	14.98	0.01
T_6	49	400	0.96	24.17	0.01

Figures 5(a) shows AUR and CMR of each task under increasing total UD of gMUA. For a task with step TUFs, task-level AUR and CMR are identical, as satisfying the critical time implies the accrual of a constant utility. But the system-level AUR and CMR are different as satisfying the critical time of each task does not always yield the same amount of utility.

Figure 5(a) shows that all tasks under gMUA accrue 100% AUR and CMR within the global EDF's bound (i.e., UD$<\approx$2.5 here), thus satisfying the desired $\{\nu_i, \rho_i\} = \{1, 0.96\}, \forall i$. This validates Theorem 3.



(a) Task-level (b) System-level

Figure 6: Performance Under Statistical Demand, Heterogenous TUFs

Under the condition beyond what Theorem 3 indicates, gMUA achieves graceful performance degradation in both AUR and CMR in Figure 5(b), as the previous experiment in Section 5.1 implies. In Figure 5(a), gMUA achieves 100% AUR and CMR for T_1 over all range of UD. This is because, T_1 has a step TUF with higher height. Thus, gMUA favors T_1 over others to obtain more utility when it cannot satisfy the critical time of all tasks.

According to Theorem 4, the system-level AUR must be at least 96%. (For each task $T_i, \nu_i = 1$, because all TUFs are step shaped.) We observe that AUR and CMR of gMUA under the condition of Theorem 4 are above 99.0%. This validates Theorem 4.

A similar trend is observed in Figure 6 for heterogeneous TUFs. We assign step TUFs for T_1 and T_4, linearly decreasing TUFs for T_2 and T_5, and parabolic TUFs for T_3 and T_6. For each task T_i, ν_i is set as $\{1.0, 0.1, 0.1, 1.0, 0.1, 0.1\}$.

According to Theorem 4, the system-level AUR must be at least $0.96 \times (400/25 + 100 \times 0.1/28 + 20 \times 0.1/49 + 100/49 + 30 \times 0.1/41 + 400 \times 0.1/49)/(400/25 + 100/28 + 20/49 + 100/49 + 30/41 + 400/49) = 62.5\%$. In Figure 6, we observe that the system-level AUR under gMUA is above 62.5%. This further validates Theorem 4 for non step-shaped TUFs. We also observe that the system-level AUR and CMR of gMUA degrade gracefully, since gMUA favors as many feasible, high PUD tasks as possible.

6 Conclusions and Future Work

We present a global UA scheduling algorithm for SMPs, called gMUA. The algorithm considers tasks that are subject to TUF time constraints, variable execution time demands, and resource overloads. gMUA considers the two-fold scheduling objective of probabilistically satisfying utility lower bounds for each task and maximizing the total accrued utility.

We establish that gMUA achieves optimal total utility for the special case of step TUFs and total task utilization demand not exceeding EDF's schedulable utilization bound, probabilistically satisfies task utility lower bounds, and lower bounds system-wide total accrued utility. We also show that the algorithm's utility lower bound satisfactions have bounded sensitivity to variations in execution time demand estimates, and that the algorithm is robust against a variant of the Dhall effect. When task utility lower bounds cannot be satisfied (due to increased utilization demand), gMUA maximizes total utility, while gracefully degrading timeliness. Our simulation experiments validate our analytical results and confirm the algorithm's effectiveness and superiority. Our method of transforming task stochastic demand into actual execution time allocation is independent of gMUA and can be applied in other algorithmic contexts, where similar (stochastic scheduling) problem arises.

Several aspects of our work are directions for further research. Examples include relaxing the sporadic task arrival model to allow a stronger adversary (e.g., the unimodal arbitrary arrival model), allowing greater task utilizations for satisfying utility lower bounds, and reducing the algorithm overhead.

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